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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: : Docket No.: IL920030060US1
Monica Farkash, et al. : Examiner:
Serial No.: 10/813,239 : Group Art Unit: 2825
Filed: March 30, 2004
For: **DISTRIBUTED BDD REORDERING**

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Commissioner for Patents
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: Confirmation No.: 4828

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: Date: September 8, 2004

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under 37 C.F.R. 1.97(b)(3)**

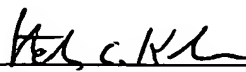
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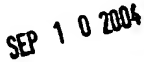
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Respectfully submitted,
Attorney for the Applicant(s)


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Application Number	10/813239
Filing Date	30-Mar-2004
First Named Inventor	Farkash, Monica
Art Unit	2825
Examiner Name	
Attorney Docket Number	IL920030060US1

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First Named Inventor	Farkash, Monica
Art Unit	2825
Examiner Name	
Attorney Docket Number	IL920030060US1

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	D	Rudell in "Dynamic Variable Ordering for Ordered Binary Decision Diagrams," Proceedings of the IEEE International Conference on Computer-Aided Design (Santa Clara, California,	
	E	Song and Chang "A Variable Reordering Method for Fast Optimization of Binary Decision Diagrams," Proceedings of the Fifth Asian Test Symposium ATS'97 (IEEE, 1997), pp. 228	
	F	Bryant and Meinel "Ordered Binary Decision Diagrams in Electronic Design Automation," published in Logic Synthesis and Verification (Kluwer Academic Publishers, 2001), pages	
	G	Meinel and Slobodova "Speeding Up Variable Reordering of OBDDs," Proceedings of the International Conference on Computer Design (1997), pages 338-343	
	H	Beer et al., in "RuleBase: an Industry-Oriented Formal Verification Tool," Proceedings of the Design Automation Conference DAC'96 (Las Vegas, Nevada, 1996)	

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